

**REMARKS**

Claims 1-28 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding rejections are respectfully requested in light of the above amendments and following remarks.

Claims 1-7 and 17-27 were rejected under 35 U.S.C. § 102(b) as being anticipated by Traylor.

These rejections are respectfully traversed.

It is respectfully submitted that Traylor does not disclose or suggest the presently claimed invention including the circuitry for evaluating continuous selected bits in the sequence in the write register relative to complimentary continuous selected bits in the sequence in the read register for detecting a level of the data fullness in the memory structure in independent Claim 1, albeit defined as the step of evaluating continuous selected bits in the sequence in the write register relative to complimentary continuous selected bits in the sequence in the read register for detecting a level of data fullness in the memory structure in independent Claim 21.

Traylor discloses that a full flag may be generated by comparing active word write word lines to the read pointer through a single row look ahead. A match indicates that the write pointer has caught up to the read pointer and sets the full flag.

Furthermore, an empty flag may be generated by comparing active read word lines to the write pointer through a single row look ahead.

A match indicates that the read pointer has caught up with the write pointer and sets the empty flag.

Consequently, Traylor does not disclose or suggest the claimed subject matter.

Applicants appreciate the indication that if Claims 8, 12, 15, and 28 were amended to include the limitations of the base claim and any intervening claims that these claims would be allowable.

Claims 8, 12, and 15 have been amended to include the limitations of Claim 1 while Claim 28 has been amended to include the limitations of Claim 21.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



W. Daniel Swayze, Jr.  
Attorney for Applicant  
Reg. No. 34,478

Texas Instruments Incorporated  
P.O. Box 655474, MS 3999  
Dallas, TX 75265  
(972) 917-5633